



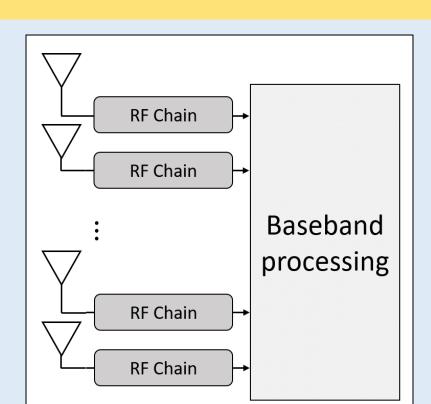


Theoretical Background

Main Contributions

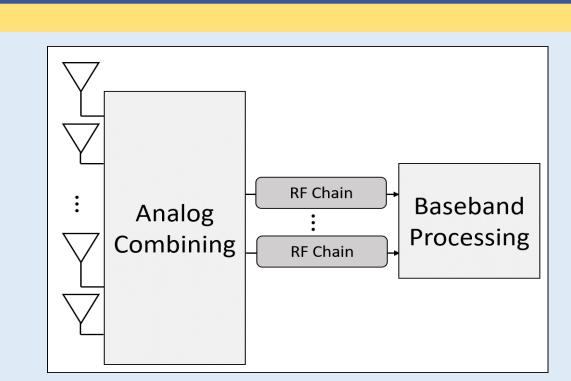
- This prototype demonstrates a MIMO receiver with reduced number of RF chains
- Employs 4 antennas and only 2 RF chains
- Demonstrates an analog combiner that consists of controllable analog network of phase shifters, gains and switches
- Performs MMSE channel estimation for Kronecker channel model in a multi-user MIMO scenario
- When the channel matrix is low rank the RF reduction does not increase the channel's MSE

Conventional MIMO Receiver



- Each antennas is followed by a dedicated RF chain
- All the analog inputs from the antennas are accessible at the baseband
- All the signal processing operations are performed in baseband (fully-digital)

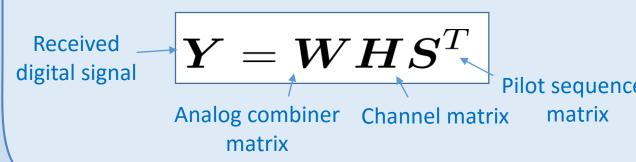
Hybrid MIMO Receiver



- An analog combiner projects the high number of analog inputs from the antennas onto the low number of RF chains
- The analog combiner consists of a controllable network of phase shifters, gains and switches
- Only a low number of measurements are accessible in baseband

Problem Formulation

- Multi-user MIMO scenario with 2 user terminals and a base station with 4 antennas and 2 RF chains
- The users transmit known, orthogonal pilot sequences of length τ to the base station, over a TDD uplink channel
- Received signal at the baseband



Kronecker channel model:

$$oldsymbol{H} = oldsymbol{R}_r^{rac{1}{2}} ar{oldsymbol{H}} oldsymbol{R}_t^{rac{1}{2}}$$

correlation with entries $\sim \mathcal{N}(0,1)$ Vectorized received signal:

$$oldsymbol{y} = (oldsymbol{S} \otimes oldsymbol{W}) oldsymbol{h}$$

- $\sim \mathcal{N}(\mathbf{0}, \mathbf{R}_t \otimes \mathbf{R}_r)$
- MMSE channel estimator
- $\left|\hat{oldsymbol{h}} = \left[oldsymbol{R}_t oldsymbol{S}^* \left(oldsymbol{S} oldsymbol{R}_t oldsymbol{S}^*
 ight)^{-1} \otimes oldsymbol{R}_r oldsymbol{W}^* \left(oldsymbol{W} oldsymbol{R}_r oldsymbol{W}^*
 ight)^{-1}
 ight] oldsymbol{y}
 ight|$

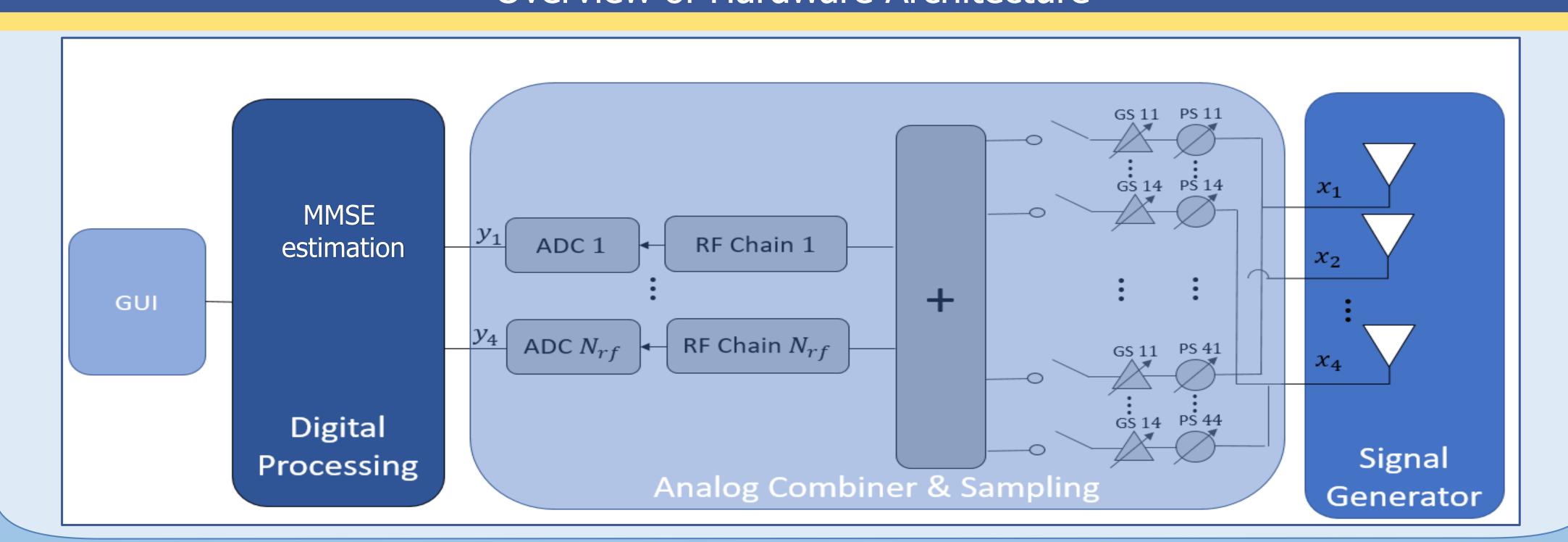
Optimal Analog Combiner

- Due to the separable structure of the Kronecker model, an optimal analog combiner can be derived.
- [Stein and Eldar '2018]

$$oldsymbol{W}_{opt} = oldsymbol{T} oldsymbol{U}_1^*$$

- U_1 First 2 eigenvectors of R_r
- T Any 2×2 invertible matrix

Overview of Hardware Architecture









Channel Estimation with Reduced RF Chains

Technical Specification

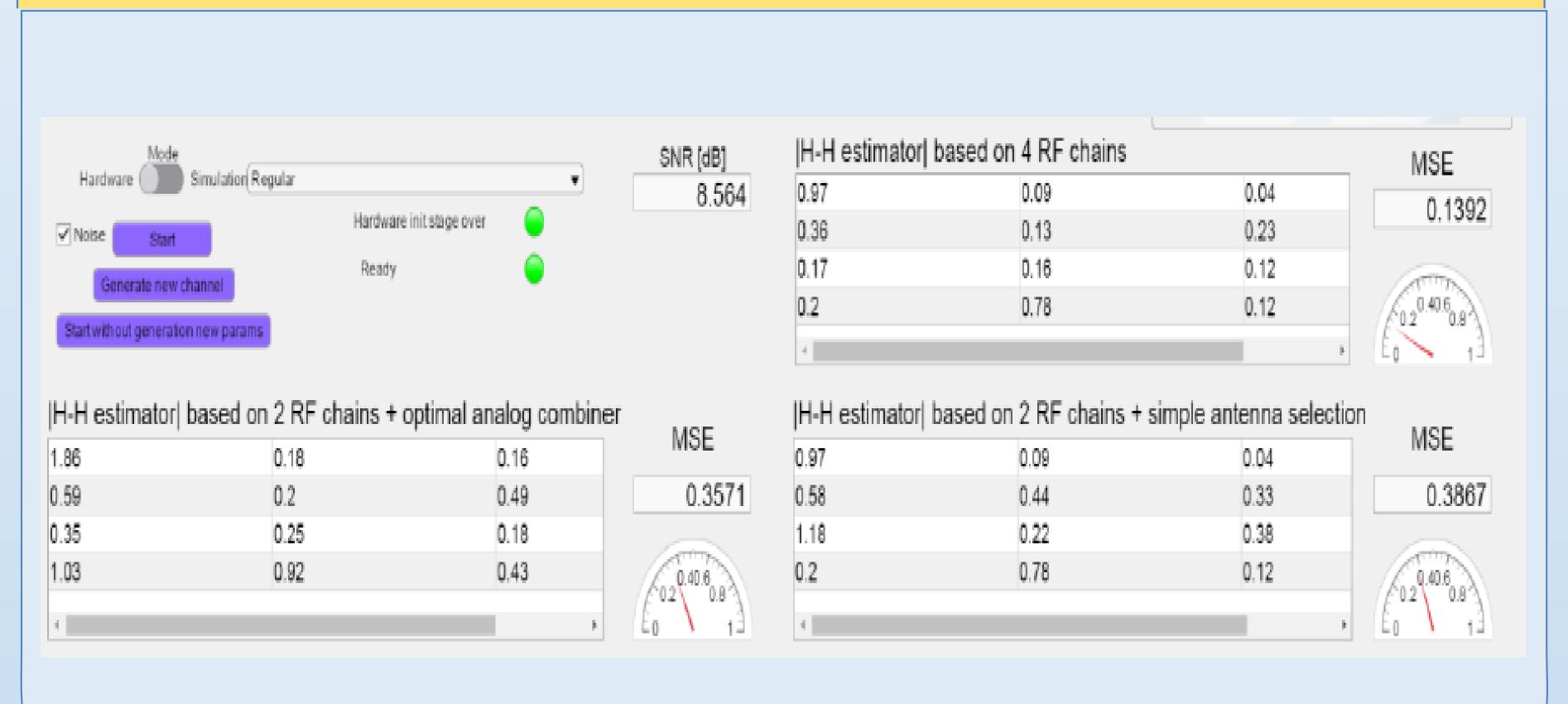
Carrier Frequency - $f_c = 1GHz$

Baseband BW = 125MHz – can be extended up to 2GHz

DAC - 4 output channels at 250MSPS

ADC – 4 input channels Sampling Rate – 250MHz for each channel

Measurement Result



Theoretical MSE

• For the suggested combiner W_{opt} the theoretic MSE in a noise free scenario is the sum of 2 smallest eigenvalues of the receive-side correlation:

$$\mathbb{E}\left[\|\hat{\boldsymbol{H}}-\boldsymbol{H}\|^2\right] = \lambda_3\left(\boldsymbol{R}_r\right) + \lambda_4\left(\boldsymbol{R}_r\right)$$

• Best case scenario: $rank(R_r) \le 2$. In this case the MSE is:

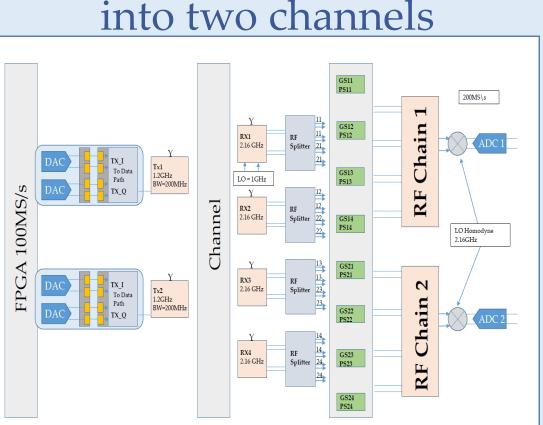
 $\mathbb{E}\left[\|\hat{\boldsymbol{H}} - \boldsymbol{H}\|^2\right] = 0$

Worst case scenario: $\lambda_1(R_r) = \lambda_2(R_r) = \lambda_3(R_r) = \lambda_4(R_r) = a$. In this case the optimal combining scheme is a simple "antenna-selection" scheme, and the MSE is:

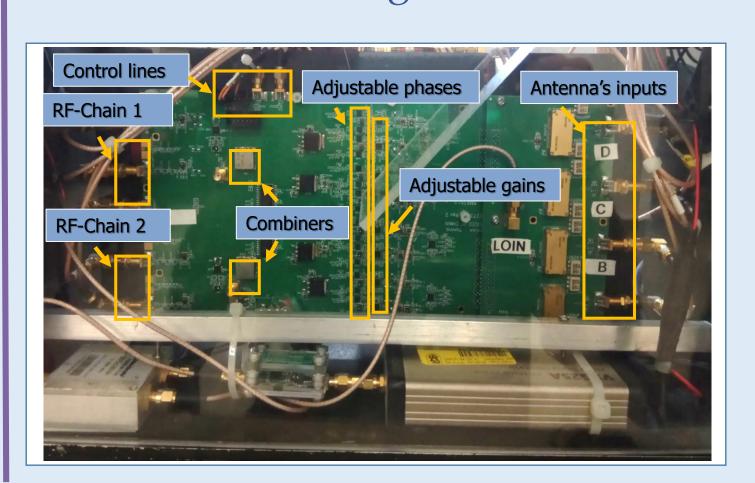
$$\mathbb{E}\left[\|\hat{\boldsymbol{H}} - \boldsymbol{H}\|^2\right] = 2a$$

Analog Pre-Processor (APP) Board

APP filters the receiver data coming from four channels into two channels



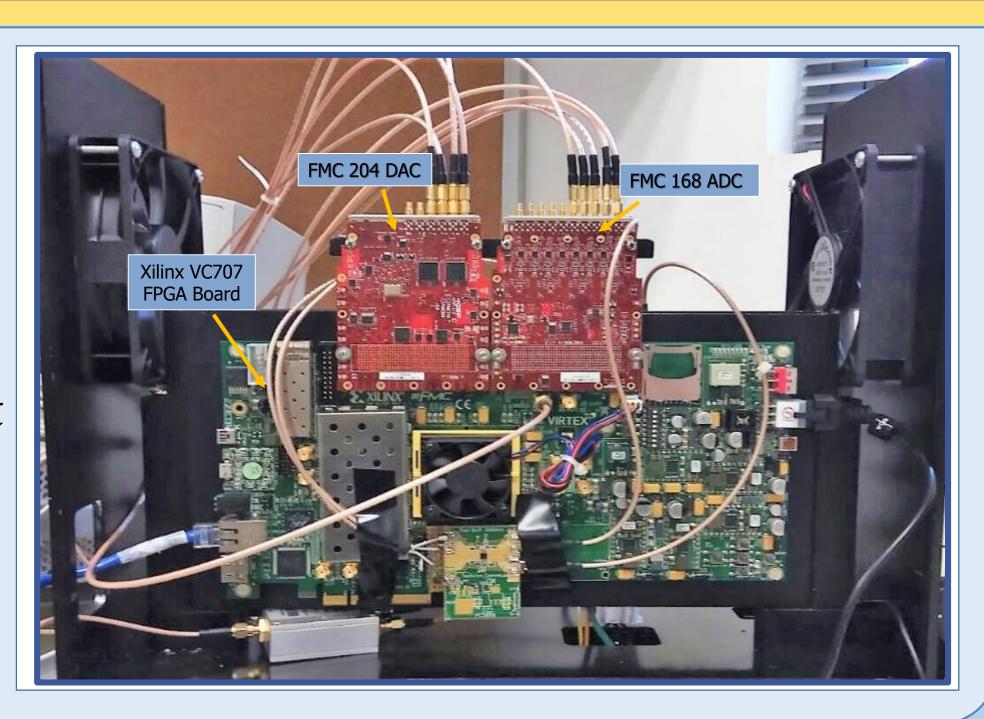
APP in a single chassis



Phase shifters and Amplitude shifters easily configured by FPGA controller

Digital Receiver and Waveform Generator

- 16-bit 8-channel digitizer
- 16-bit 4-channel DAC for Waveform Generation
- Analog signal phase shift resolution 0.5°
- Amplitude modulation



User Interface

